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PATENT
Docket No.: ST02009CI1 (245-US-CIP1)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: SiRF Technology, Inc.

Title: Serial Radio Frequency
to Baseband Interface
with Power Control

Serial No.: 10/632,051

Filed: July 30, 2003

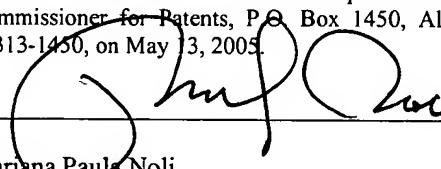
First Named Inventor: Gronemeyer, Steve et al.

Art Unit: 3662

Examiner: Not Yet Assigned

CERTIFICATE OF MAILING

I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Services with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 3, 2005.


Mariana Paula Noli

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Commissioner:

The information disclosure statement submitted herewith is being filed within three months of the mailing date of the International Search Report issued in the corresponding PCT application and before the mailing of a first office action. 37 C.F.R. §1.97(b).

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 C.F.R. § 1.97(g)), an admission that the

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information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

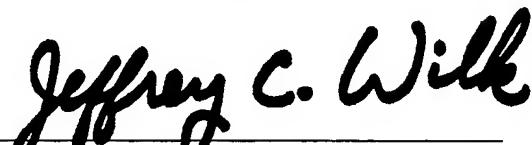
Copies of the non-patent documents, foreign patents and foreign patent publications cited as references are enclosed.

Respectfully submitted,

THE ECLIPSE GROUP

Date: 5/13/05

By: _____



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FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office List of Documents Cited by Applicant		Attorney Docket No.: ST02009CI1 (245-US-CIP1)	Serial No.: 10/632,051
		Applicant(s): Gronemeyer, Steve et al.	
		Filing Date: 07/30/2003	Group: 3662

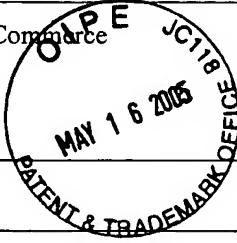
U.S. PATENT DOCUMENTS

Examiner Initials	No.	Document Number	Date	Name	Class	Subclass	Filing date if Appropriate
	01	6,028,887 A	02/22/2000	Harrison et al.			
	02	2002/0132648 A1	09/19/2002	Kerth et al.			
	03	2002/0141511 A1	10/03/2002	Vishakhadatta et al.			
	04	2002/0142741 A1	10/03/2002	Molnar et al.			
	05	2002/0168951 A1	11/14/2002	Paulus et al.			
	06	2002/0128942 A1	11/14/2002	Vishakhadatta et al.			
	07	2002/0168952 A1	11/14/2002	Vishakhadatta et al.			
	08	2002/0172263 A1	11/21/2002	Kindred et al.			
	09	2002/0187763 A1	12/12/2002	Lim et al.			
	10	2002/0193084 A1	12/19/2002	Lim et al.			
	11	2002/0193140 A1	12/19/2002	Behrens et al.			
	12	2003/0003887 A1	01/02/2003	Lim et al.			
	13	2003/0013428 A1	01/16/2003	Marquez et al.			
	14	2003/0017809 A1	01/23/2003	Garlepp et al.			
	15	2003/0063690 A1	04/03/2003	Paulus et al.			
	16	2003/0232613 A1	12/18/2003	Kerth et al.			
	17	2004/0077327 A1	04/22/2004	Lim et al.			
	18	2004/0166822 A1	08/26/2004	Gronemeyer, Steve et al.			

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		Applicant(s): Gronemeyer, Steve et al.	
		Filing Date: 07/30/2003	Group: 3662

FOREIGN PATENT DOCUMENTS

Examiner Initials	No.	Document Number	Date	Name of Patentee or Applicant	Country	Translation Yes No
	19	WO 97/14053	04/17/1997	Snaptrack, Inc.		
	20	WO 02/056487	07/18/2002	Silicon Laboratories, Inc.		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initials	No.	Full Information Of Document
	21	New Fast GPS Code-Acquisition Using FFT, Electronic Letters, Vol. 27, Number 2, pages 158-159 (1991)
	22	Novel Fast GPS/GLONASS Code Acquisition Technique Using Low Update Rate FFT, Electronic Letters, Vol. 28, Number 9, pages 863-865 (1992)
	23	A Low Cost GPS Receiver for In-Vehicle Navigation, Steven Moore, 10 pages
	24	SiGe Semiconductor SE4100L data sheet, 16 pages (August 6, 2002)
	25	SiRFStar® GPS Architecture GRF1/LXi data sheet, 16 pages (February 2001)
	26	SiRFStar® GPS Architecture GSP1/LX data sheet, 52 pages (March 31, 2000)
	27	SiRFStar® Ile Chip Set Architecture Sheet, 2 pages (January 2002)
	28	SiRFStar® Ile/LP Chip Set Architecture Sheet, 2 pages, (February 2002)
	29	SiRFStar® IIt Chip Set Architecture Sheet, 2 pages (January 2002)

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